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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/582,287	09/08/2000	Yoshiyuki Kamihara	106385	4264

25944 7590 06/15/2004

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EXAMINER

FLEMING, FRITZ M

ART UNIT	PAPER NUMBER
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2182

DATE MAILED: 06/15/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/582,287

Applicant(s)

KAMIHARA ET AL.

Examiner

Fritz M Fleming

Art Unit

2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 23 April 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-34 is/are pending in the application.
- 4a) Of the above claim(s) 11-24, 26-28, 30-32 and 34 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10, 25, 29 and 33 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

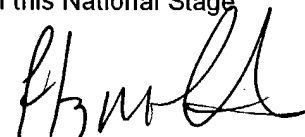
## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 September 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

  
FRITZ FLEMING  
PRIMARY EXAMINER  
GROUP 2100

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 23 June 2000, 29 March 2002,
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

31 December 2003

Office Action Summary

Part of Paper No./Mail Date 20040614

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***Election/Restrictions***

1. Applicant's election with traverse of claims 1-10,25,29 and 33 in the reply filed on 04/23/04 is acknowledged. The traversal is on the ground(s) that there is no burden to search multiple inventions. This is not found persuasive because the restriction requirement also set forth details of the various groups based upon PCT Rule 13.1, as this is the proper basis for determining the restriction requirement. Applicant did not traverse the requirement as set forth by the examiner, hence the response was not persuasive.

The requirement is still deemed proper and is therefore made FINAL.

***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-10,25,29,33 are rejected under 35 U.S.C. 102(b) as being anticipated by EP 0 804 008.

Note that the ASIC 20 performs the controlling of the transfer of data packets between the devices of the PCI bus 24 and the various nodes of the 1394 bus at 14 via the 3 port physical layer I/F 18. Note the link means 90 to ultimately transfer packets amongst the various nodes. Figure 20 shows packet assembly means, in which there are means for reading control information, from for example, a RAM area of PCL 0 (456), and a data portion of the RAM as shown

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by data buffers 460-464, wherein an upper layer, i.e. the link layer writes in to these areas, as seen in Figure 26b with the dual port RAM 501 with control at mux 518 and data at 534. Data pointers are seen at 524 and 526, which come from 1394 transmitter control. Error checking is performed in order to determine if a packet status is valid, and also by 20 for CRC. Pointers are updated throughout the operation of Figure 20 and 26, in order to keep up with the commanded data transfers. Likewise, control information is created and written during fetching at the loop described at page 17, line 33+, as well as the starting of transmission based upon the completion of such. As far as busses are concerned, note the busses of the 1394 serial bus connected to next stage applications at 14, a second bus 24 for controlling the data transfer device, a third bus per Figure 2 connecting 90 to 78, and a fourth bus connected to 78 and 58, wherein the ASIC 20 performs arbitration to establish the data paths to and from the 1394 devices and the PCI bus. As far as claim 33 is concerned, the devices, for example, connected to the 1394 bus provide data (camera 52) to be processed, with the processed data then output or stored at the remaining 1394 bus devices.

### ***Conclusion***

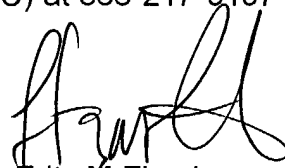
4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The remaining art is cited to show IEEE 1394 packet assembly.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fritz M Fleming whose telephone number is 703-308-1483. The examiner can normally be reached on 9-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on 703-308-1483. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Fritz M Fleming  
Primary Examiner  
Art Unit 2182

fmf

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